



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Kyle K. Kirby

Serial No.: 10/673,692

Filed: September 29, 2003

For: METHOD FOR CREATING
ELECTRICAL PATHWAYS FOR
SEMICONDUCTOR DEVICE
STRUCTURES USING LASER
MACHINING PROCESSES

Confirmation No.: 4168

Examiner: M. Estrada

Group Art Unit: 2823

Attorney Docket No.: 2269-5665US
(02-1291.00/US)

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AMENDMENT UNDER 37 C.F.R. §1.116

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Sir:

The following amendments and remarks are filed in response to the Examiner's remarks in the Final Office Action mailed February 14, 2007, the three-month shortened statutory period for response to which expires on May 14, 2007. This response is submitted on or before April 16, 2007, April 14, 2007, which is two months from the mailing date of the Final Office Action, being a Saturday.